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	First Named Inventor	GRUI	DIN, Oleg	
	Art Unit		3729	
(Not for Submission under 07 of K 1.00)	Examiner Name	PHAN	I, Thiem D.	
	Attorney Docket Number		14836-14US AD/ad	

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	2	Y. Amemiya, T. Ono, K. Kato: "Electrical Trimming of Heavily Doped Polycrystalline Silicon Resistors", IEEE Trans. Electron. Dev. vol. ED-26 (1979), 11, pp.1738-1742										
	3	J. Babcock, P. Francis, R. Bashir, A. Kabir, D. Shroder, M. Lee, T. Dhayagude, W. Yindeepol, S. Prasad, A. Kalnitskiy, M. Thomas, H. Haggag, K. Egan, A. Bergemont, P. Jansen, "Precision Electrical Trimming of very Low TCR Poly-SiGe Resistors", IEEE Electron. Dev. Letters, vol. 21 (2000), 6, pp.283-285										
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